REMARKS/ARGUMENTS

Claims 12-22 are now pending in this application. Claims 1-11 have been canceled without prejudice or disclaimer. No new matter has been added.

Claim Rejections under 35 U.S.C. §102

Claims 1-11 have been rejected under 35 U.S.C. §102(b) as being anticipated by Yamakawa et al., U.S. Patent No. 6,134,960, however the rejection is rendered moot by the cancellation of the claims without prejudice or disclaimer. New claims 12-22 are patentable over Yamakawa et al., U.S. Patent No. 6,134,960 and the remainder of the art of record for the following reasons.

Claims 12-22 are directed to the same invention as claims 1-11. Support for new claims is provided by the specification and drawings, and no new matter has been added. Figs. 1 and 2 show circuit constituting conductor members 3, 4 that are on a substrate 1. Covering at least part of the conductors 3, 4 is an insulating member 5. An amplifying means (Op) is provided on the same side of the substrate 1 as the conductors members 3, 4 and the insulating member 5. As set forth in claim 1, the amplifying means is electrically connected to the circuit-constituting conductors and the amplifying means amplifies the signal inputted thereto via the conductor members and outputs an amplified signal. Further, at least a part of the surface of the circuit-constituting conductor member is covered by conductor (2) via the insulating member (5) and the conductor 2 is not electrically connected to any of the other conductors. The first and second signal lines of claims 13-15 are supported by conductor members 3, 4 respectively. Support for the claims including a thermal flow meter is additionally provided by Figs. 3 and 4.

According to the present invention, a circuit board for an electronic circuit includes a conductor layer for the circuit that is formed on an insulated board. Conductors 3, 4 are connected to the positive and negative input terminals of an operational amplifier of a circuit portion that is allocated on a conductor layer of the circuit and formed of a monolithic IC. A conductor 2 covers the conductors 3, 4 via the insulating member 5, and the conductor 2 is not connected to other conductors. As a result, electromagnetic interference is prevented because the conductor 2 capacitively couples conductors 3, 4 to terminate the AC elements, thereby preventing electromagnetic interference. See the paragraph bridging pages 6 and 7 of the specification.

Yamakawa does not disclose or suggest the invention as claimed. In particular, Yamakawa does not disclose a conductor that covers, via an insulating protective film (3 in Yamakawa), circuit constituting conductors, such as the conductors of the heat-generating resistors 4 and 5, wherein the conductor is not electrically connected with any other conductor or part as claimed. See, for example, Figs. 1B, 2B, 3B, and 4B of Yamakawa, which show an insulating layer 3 as a top layer. Accordingly, new claims 12-22 are patentable over Yamakawa and the remainder of the art of record.

Information Disclosure Statement

Applicants appreciate receiving the initialed PTO-1449 Form with the Office Action. However, Applicants have noticed that the Form PTO-1449 that has been returned requires correction in that the references "AM" and "AN" are publications of Japan, not the EPO. Accordingly, Applicants submit herewith a corrected PTO-1449 Form and request that the Examiner initial and return the corrected form, and in addition, replace the previously filed

form with the corrected one to ensure that the references are made of record.

CONCLUSION

In view of the foregoing, Applicant respectfully requests that a timely Notice of Allowance be issued in this case.

Respectfully submitted,

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JRM/so
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